Electionic Pate	nt Application Fee Transmittal			
Application Number:	10709677			
Filing Date:	21-May-2004			
Title of Invention:	SYSTEM AND METHOD FOR PROTECTING AGAINST SHORT CIRCUITS IN ELECTRIC POWER DISTRIBUTION ARCHITECTURES WITH TWO VOLTAGE LEVELS			
First Named Inventor/Applicant Name:	Carles Borrego Bel			
Filer:	Martin J. Sultana/Lorraine Schneider			
Attorney Docket Number:	LEAR 8153ES PUSA			
Filed as Large Entity				
Utility Filing Fees				
Description	Fee Code Quantity Amount Sub-Total USD(\$)			
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Request for continued examination	1801	1	810	810
Submission- Information Disclosure Stmt	1806	1	180	180
	al in USD	(\$)	990	